

Notice of Allowability

Application No.

10/627,179

Examiner

Kevin Quarterman

Applicant(s)

YONEDA ET AL.

Art Unit

2879

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 24 July 2003.
2. ☒ The allowed claim(s) is/are 1-15.
3. ☒ The drawings filed on 24 July 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
 - * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 0703
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.
2. The application has been amended as follows: Please delete current title and insert the following as a new title: --ELECTROLUMINESCENCE DISPLAY APPARATUS WITH OPENING IN SILICON OXIDE LAYER--.

Allowable Subject Matter

3. Claims 1-15 are allowed.
4. The following is an examiner's statement of reasons for allowance: Regarding independent claim 1, the prior art of record neither shows or suggests an electroluminescence display apparatus comprising, in addition to other limitations of the claim, a silicon oxide layer having an opening in a region where a thin film transistor is not formed. Due to their dependency upon independent claim 1, claims 2-8 are also allowable.
5. Regarding independent claims 9 and 10, the prior art of record neither shows or suggests an electroluminescence display apparatus comprising, in addition to other limitations of the claims, a silicon oxide layer of a thin film transistor having opening at a position under an emissive region of an electroluminescence element. Due to their dependency upon independent claim 10, claims 11-15 are also allowable.

6. The subject silicon oxide layer with an opening, as discussed earlier, is provided for increasing the light emission efficiency of the display apparatus and increasing the display contrast. The design is new and unique to the art.

7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Kawasaki (US 6281552) discloses thin film transistors having LDD regions. Ohtani (US 6277679) discloses a method of manufacturing thin film transistors. Yamazaki (US 6365917) discloses a semiconductor device. Nakajima (US 6420758) discloses a semiconductor device having an impurity region overlapping a gate electrode. Yamauchi (US 6512504) discloses an electronic device.

Contact Information


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Quarterman whose telephone number is (571) 272-2461. The examiner can normally be reached on M-TH (7-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin Quarterman
Examiner
Art Unit 2879

kq 
9 September 2004


Joseph Williams
Primary Examiner
Art Unit 2879